INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

	Complete if Known	
Application Number	10/687,944	
Filing Date	October 17, 2003	
First Named Inventor	P.L. Connor	
Art Unit	2182	
Examiner Name	Not Yet Assigned	

Page	1	of	1	**Attorney Docket Number**	P16579

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number- Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
c*fr*		US-2003/0110281	06/12/2003	Minnick et al.	
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code- Number- Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
C*fL*		"Method for Operation-Based Adaptive Transmit Interrupt Moderation", *IP.com*, 25066, DISCLOSED ANONYMOUSLY.	
C*fr*		"Select Interrupt Assertion of Egress Events", *IP.com*, 23175, DISCLOSED ANONYMOUSLY.	
C*fr*		U.S. Patent Application Serial No. 09/409,626, filed September 30, 1999, with Intel Docket No. P7089, entitled "Reducing Network Interrupts", invented by R. D. DUNLAP, G. D. CUMMINGS, P. L. CONNOR, G. G. LI, & J. A. RONCIAK.	
C*fr*		U.S. Patent Application Serial No. 09/955,370, filed September 17, 2001, with Intel Docket No. P12273, entitled "METHODS AND APPARATUS FOR REDUCING RECEIVE INTERRUPTS VIA PACED INGRESS INDICATION", invented by P. J. LUHMANN & P. L. CONNOR.	
c*fr*		U.S. Patent Application Serial No. 09/967,300, filed September 27, 2001, with Intel Docket No. P11381, entitled "Apparatus and Method for Packet Ingress Interrupt Moderation", invented by P. L. CONNOR.	
c*fr*		U.S. Patent Application Serial No. 10/055,922, filed January 28, 2002, with Intel Docket No. P12819, entitled "Ingress Throttling Via Adaptive Interrupt Delay Scheduling", invented by P. L. CONNOR, D. GAUR, E. MANN, G. TSAO, & M. GIBSON.	
C*fr*		U.S. Patent Application Serial No. 10/326,740, filed December 20, 2002, with Intel Docket No. P14972, entitled "Method, System, and Article of Manufacture for Adjusting Interrupt Levels", invented by P. L. CONNOR, E. K. MANN, H. T. TRAN, P. GOVINDARAJAN, J. P. JACOBS, D. M. DURHAM, G. D. GUMANOW, & C. Y. CHIU.	
d*fV*		U.S. Patent Application Serial No. 10/464,512, filed June 19, 2003, with Intel Docket No. P16544, entitled "Bandwidth Conserving Protocol for Command-Response Bus System", invented by P. L. CONNOR.	

Examiner Signature	*Chm Knach*	Date Considered	07/18/2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.